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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

A N	APPL NUM 10017670	FILING DATE 12/12/2001	CLASS 439	SUBCLASS 29	GAU 2729 2833	EXAMINER <i>D. Nguyen</i>
**APPLICANTS: Hetherton Scott; Montoya Wayne; Bruguier Thomas; Daering Randy; Toth James; Chandler Daniel; Galla Matthew;						
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CIP OF 09/395,869 09/14/1999 AND IS A CIP OF PCT/US00/25118 09/13/2000						
<b>** FOREIGN APPLICATIONS VERIFIED:</b>						
PG-PUB	<input type="checkbox"/> DO NOT PUBLISH		<input type="checkbox"/> RESCIND			
Foreign priority claimed			<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met			<input type="checkbox"/> yes <input type="checkbox"/> no		MP1705-US2	
Verified and Acknowledged Examiner's initials						
TITLE : Electrical devices and process for making such devices						
U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)						

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NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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